NATIONAL RADIO ASTRONOMY OBSERVATORY SOCORRO, NEW MEXICO VERY LARGE ARRAY PROGRAM

VLA COMPUTER MEMORANDUM NO. 155

AN IMPROVED VERSION OF THE ON-LINE DATA CHECKING ALGORITHM CHKDEF

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February 1980

The large volume of data produced by the VLA requires an automatic means of ascertaining data quality. While at the present time manual editing does occur for continuum observations, it will become an increasingly odious and time-consuming task as more antennas come on-line. With the advent of the spectral line system, manual editing will be out of the question, and a properly functioning, automatic editing scheme will be required. The principle mechanism for providing data flagging is the on-line program CHK which runs in the Modcomp computer MONTY. The fact that manual editing of VLA data is still required points to inadequacies of this program.

For the past several months we have been working to bring on-line an improved version of the data checking algorithm CHKDEF. These modifications involve improved specifications of instrumental parameters logged in data sets 1, 2 and 5, and the implementation of checks on quantities not previously scrutinized. These modifications are possible because of information supplied by Larry D'Addario and Peter Napier on the appropriate operating characteristics of various pieces of equipment in the VLA.

The purpose of this memo is twofold; to announce the implementation of a new version of CHKDEF which will hopefully perform more effectively than its predecessor, and to document the current status of the on-line CHECKER system. We begin by presenting a brief description of the system. The program CHK operates on "monitor points". Each monitor point records a signal from a piece of equipment in the VLA. Some examples of monitor points are power supply voltages, cal and total power readings, current drawn by paramps, device temperatures, transmitted and received IF powers, etc. The monitor points are organized into 6 "data sets", which consist of a set of monitor points specific to some VLA subsystem. Table I lists the 6 data sets. Listed are the data set number, subsystem monitored, and the number of monitor points in the data set.

TABLE I DCS DATA SETS

Number	Subsystem	Number of	Monitor Points
0	antenna control unit		12
1	front end		80
2	antenna LO and modem		
	controls		77
3	subreflector control		19
4	clipper/sampler + gains/	phases	24
5	local LO and modem cont	trois	45

Thus there are provisions for checking 257 monitor points per antenna. In addition to the monitor points tallied in Table I, each data set has a number of digital words which indicate status of the system, e.g., band selected. Some of the monitor points are spares and others are not checked, but Table I gives an idea of the degree of usage of the DCS system.

The program CHK accesses a subset of the monitor points, and checks whether values are within specifications. If not, a flag of one-of-four severity levels is set, and an appropriate message is fetched from a message file and printed on a line printer in the control room. The flags set by CHK range from 1 (data probably

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OK) to 4 (data useless). The decisions concerning flag level specification are to some extent arbitrary.

The scheme by which Modcomp flags are converted to the data base flags accessible to the observer is given in Table II.

TABLE II

MODCOMP TO DEC-10 FLAG CONVENTION SCHEME			
CHK Flag	Modcomp Code	DEC-10 Code	Proposed
0	0	0	0
1	1	1	0
2	2	2	0
3	4	3	2
4	8	3	2

The results of our work on data sets 1, 2 and 5 are given in Tables III-V, respectively. The format for all three tables is the same, the first column gives the monitor point number and column 2 lists a brief description of the signal checked. Column 3 gives the "tolerance", the tolerance defined such that if the inequality listed in this column is satisfied, a flag is set. Column 4 gives the flag severity level as defined above. A few comments are in order with regard to the notation employed in column 3. For most cases, an obvious inequality with respect to some voltage level is specified. Slanting brackets (/) indicate absolute values, and numbers separated by a colon conform to the convention, Data Set Number:Monitor Point Number in octal. As an example, /1:107-1:160/>0.2 V means that if the absolute value of the difference of monitor points 107 and 160 in data set 1 exceeds 0.2 V, a flag is set.

Tables III-V are on the DEC-10 disk in the 13 area of Spangler. This availability will facilitate updating as the CHECKER system evolves, and provides ease in distribution of this information. The

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file names for Tables III-V are, respectively, DS1.TB, DS2.TB and DS3.TB. Spangler will maintain and update these files as the system changes. Each file contains in the title line the date of last modification.

Future work in this area will consist of tests of the effectiveness of the present CHECKER system, and hopefully the determination of what monitor point tolerances need to be adjusted and what additional devices need to be monitored.

STATU	S OF DATA SET 1 CHECKS AS OF:	Feb. 25,1980	
***	****	****	******
Monitor	Point Signal Checked	Tolerance	Severity
ø	A Channel Cal Strength	<0.2V, <1.9V	3,2
1	A Channel Total Power	<0.7V, >4.0V	1
2	A Channel ALC	<0.5V	1
3	A Channel Peak Detector	>1.0V	2
4	********	*****	****
5	Not implemented: Same as Ø-	4 except for channel B.	•
6	Will be activated when chan	nel B becomes operation	nal
7	*******	****	****
20	********	*******	****
21	Monitor Points 20-23 are sa	me as 0=4, except for	
22	Channel C is presently i	mplemented.	
23	*****	****	****
24	********	******	****
25	Monitor Points 24-27 are sa	me as 0-4 except for ch	annel
26	D. Will be activated when C	hannel D becomes operat	ional.
27	*****	******	*****
40	F5 -15V Power Supply	<-7.65V, >-7.35V	1
Monitor	Point Signal Checked	Tolerance	Severity
41	F5 +5V Power Supply	<4.9V. >5.1V	1
42	F5 +15V Power Supply	<7.35V.>7.65V	1
43	SPARE		•
4 5	AB Pump Supply	<5.1V. >8.4V	1
45	AB Pump Voltage	/ 1:44-1:45/ <0.5V	- 2
46	CD Pump Voltage	<5.1V.>8.4V	1
47	CD Pump Supply	/ 1:46-1:47/ <0.5V	- 2
50	AB F6 Log Detector	< -3.5V.>-2.0V	1
51	CD F6 Log Detector	< -3.5V, >-2.0V	ī
52	SPARE		-
53	SPARE		
54	SPARE		
55	SPARE		
56	SPARE		
57	SPARE		
60	Dewar Temperature:0-300K	Indicator NOT CHECKE	D
61	Dewar Temperature:0-30K indic	ator >9.0v	2
62	Helium Return Pressure	NOT CHECKED	
63	Dewar Vacuum	NOT CHECKED	
64	Helium Supply Pressure	NOT CHECKED	
65	SPARE		
66	SPARE		
67	SPARE		
Monitor	Point Signal Checked	Tolerance	Severity
100	F2 PLL Tuning Voltage	<-5V, >+5V	1
101	F2 PLL Quad Phase	<0.5V, >1.5V	1,3
1:12	SPARE		-
103	SPARE		
104	SPARE		
105	F5 10 Volt Source /	1:105-1:106-10.0/ >0.01	V 1
106	Front End Ground	/ 1:106/ > 0.01V	1
1 517	Rack A Bin Temperature	<2.4V, >3.4V	1
		/ 1:107-1:160/ > 0.2V	2
120	F3 PLL In Phase	<-5.0V, >+5.0v	1

4.3.4	RO DIT Anna Dhaca	/ 1.121/ / a SV	2
121	rs PLL GUAG PHase		~
122	SPARE		
123	SPARE		
124	<u>AB</u> Pump Temperature	/ 1:124-5.0/ > 9.1	
		/ 1:124=1:124,1/ >0	0.05V 2
125	AB Heater Current	<0.2V, >9.8V	1
126	CD Pump Temperature	<4.85V, >5.15V	1
127	CD Heater Current	<0.2V, >9.8V	1
144	AB K LO Mixer Current	<-10.0V, >0.25V	2
145	AB K LO Mixer Voltage	<-1.4V, >-0.25V	1
146	AB KU LO Mixer Current	<-10.0V, >-0.25V	2
147	AB KU Mixer Voltage	<-1.4V, >-4.25V	1
150	AB Paramp 1 Bias Current	>9.0V	2
151	AB Paramp 1 Bias Voitage	<-1.1V, >1.7V	2
152	AB Paramp 2 Bias Current	>9.ØV	2
153	AB Paramp 2 Bias Voltage	<-1.1V, >1.7V	2
154	SPARE		
155	SPARE		
156	AB Upconverter Blas Curren	t >9.0V	2
157	AB Upconverter Bias Voltag	e <0V. >5.0V	2
164		***	****
165	same as 144-147, only for	CD Receiver Channel	
166	Guilte do Arrestory study so		
167	****	****	****
17/3	*******	****	***
174	Same as 1500157, only for	cD Receiver channel	
171	Same as 159-1577 Only 10.	(in necesser Chamies	
1/2			
L/3			
1/4			
1/5			
1/6			
177	*********************	੶₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩₩	****

Notes on Additional Checks and Checks on Computed Quantities

- (1) For all four channels, the average values of cal deflection and total power are used to calculate the system temperature. If the system temperature is greater than 8 times nominal, a severity level 3 flag is set.
- (2) A test for cal and system temperature fluctuations is employed as follows; the instantaneous cal and total power readings are compared with the mean over a number of previous intervals. If the cal differs from the mean by > 1/ 8 of the mean, a flag is set. If the total power differs from the mean by > 1/ 16 of the mean, a flag is set.

TABLE IV

DATA SE	I 2 CHECKS AS OF: Feb. 25,1980		
*****	*****	*****	****
Monitor	Point Signal Checked	Tolerance	Severity
0	II Cround	A GIV. SA AIV	•
¥/	5 MHZ OSCILLATOR Dewor	<1 5V	1
2	5 MHz Output Dowor	<1 5V	2
2	10 NHR Output Power	<1 5V	1
3	Te Maz Output Power	NUT CHECKED	1
4	20 MHZ Ducput rower	A SV	•
J	10 MHZ ULIVEL FOWEL	1 2+6/ 510 AV	· · · · · ·
7	SW MHZ VCO CONCION VOILAGE	NAT CHECKED	
10	50 MHZ LOOP THESE EITOL		4
11	JV MAZ LUMD POWEL		2
11	1200 MHZ PDL HOL LOCKED (111)	MOT LUFCKED	5
12	1200 MHZ PUD PHASE Error	KOI CHECKED	•
13	1200 MHZ VCO CONCIOI VOICAGE	NOT CHECKED	4
14	1800 MHZ PDD Phase Cilor		
15	1800 MHZ VCU CONLIGI VOILAGE		1
10	1200 MHZ Received Level		1
1/	1800 MHZ RECEIVED LEVEL		1
20	L3 1800 MHZ PLL not locked	>	3
21	1200 MHZ VCO OUTPUT POWER	>=10.4V	1
22	1800 MHZ VCO OUTPUT POWer	>=4,4V	1
		>5.07	. 1
23	5 MHz VCO Control Voltage	/ 2:23/ > 4.0	/ 5
24	5 MHz Phase Error	<-0.2V, >0.2V	1
		/ 2:24-2:24,1/	>0.3V 2
25	600 MHz Phase Error	NOT CHECKED	-
26	5 MHz Lock Selected (TTL)	>2.0V	3
27	Abnormal State Selected	>2.0V	3
40	T2 Ground	<-0.01V,>0.01V	/ 1
41	Temperature Monitor	NOT CHECKED	
42	Rack B Temperature Monitor	<2.2V, >3.0V	1
		/ 2:42-2:42,1/	>0.2V 2
Monitor	Point Signal Checked	Tolerance	Severity
43	vertex Room Air Cooler	NOT CHECKED	
44	Vertex Room Air Heater	<0.5V, >4.5V TL	JRNED OFF
45	transmit IF Power	<2.01	2
46	Received IF Power	<2.0V	2
47	Varactor Monitor	NOT CHECKED	-
50	Integrator Output	<-5.0V, >5.0V	1
51	Gunn Osciliator Voltage	NOT CHECKED	-
52	Gunn Oscillator Current	NOT CHECKED	
51	SPARE		
55	Warning: Not in Auto	>2.0V	1
56	Free Run Warning	NOT CHECKED	-
50 57	TI Lock Warning	>2.0V	1
100	16 Loop Amplifier Voltage=A	NOT CHECKED	*
101	Lé Tuning Voltage=A	NOT CHECKED	
102	+5 Volte Power Sunniv (A)	<5_0V_ >5_3V	1
102	=5.2 VAITE DAWAP SUBDLY (A)	C-5.2V. 3-5.0V	1
1 x' 3 1 (A A	-JE VOILS COMEL SUPPLY (N) 115 Voite Dawar Supply (N)	<->+	v t
104	•15 Volts Dower Supply (A)	<-7.65V. >7.35	V 1
1.00	-10 Volte Nowar Cupply (N)	10+ CUCCKED	• • •
100	-28 Volts Power Supply (A)	<-7.15V. >-6.85V	1
120	SPARE		-
121	SPARE		
·· ··· ·			

122 SPARE

123	SPARE
124	SPARE
125	SPARE
126	SPARE

Monitor Point Signal Checked Tólerance Severity SPARE 127 140 Loop Amplifier Voltage-C NOT CHECKED 141 NOT CHECKED Tuning Voltage=C 142 +5 Volts power Supply-C NOT CHECKED 143 -5.2 Volts Power Supply-C NOT CHECKED +15 Volts Power Supply-C -15 Volts Power Supply-C NOT CHECKED 144 NOT CHECKED 145 +20 Volts Power Supply-C NOT CHECKED +28 Volts Power Supply-C <+6.85V, >+7.15V 146 147 160 SPARE SPARE 161 162 SPARE 163 SPARE 164 SPARE 165 SPARE

NOTES FOR DATA SET 2

SPARE

SPARE

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(1) Monitor Point 24... Test for 5MHz phase jump also made

TABLE V

DATA SE	T 5 CHECKS AS OF: Feb. 25,1980		
******	****	*****	****
Monitor P	oint Signal Checked	Tolerance	Severity
0	1290 MHZ Received Level	<0.6V, >0.9V	1
1	1800 MHz Received Level	<4.67, >0.97	1
2	1200 MHz Phase Error	NOT CHECKED	•
3	1200 MHz VCO Control	<-5V, >8V	1
4	1800 MHz Phase Error	NOT CHECKED	
5	1800 MHz VCO Control	<-5V, >8V	1
6	1200 MHz VCO Power Output	<-Ø.6V	1
7	1800 MHz VCO Power Output	<-0.6V	1
12	1200 MHz Lock Indication (TTL) >1.0V (HI)	3
13	1840 MHz Lock Indication (TTL) >1.0V (HI)	3
14	78 KHz Control Voltage	<1.0V, >6.0V	1
15	L11 -15V Power Supply	<-7.6V, >-7.4V	1
16	L11 Ground	>0.01V	1
17	L11 +15V Power Supply	<7.4V, >7.6V	1
40	Ground	>0,01V	1
41	D Rack Temperature- West A	rm NOT CHECKED	
42	<u>D</u> Rack Temperature - East	ARM NOT CHECKED	
45	Transmitted IF Power	<0.8V, >2.0V	2
46	Received IF Power	<2.0V	2
47	varactor Monit ^o r	NOT CHECKED	
5Ø	Integrator Output	/2:50/ >5.0V	2
51	Gunn Oscillator Voltage	NOT CHECKED	
52	Gunn Oscillator Current	NOT CHECKED	
54	SPARE		
55	Warning:Not in Auto	>2.0V	1
56	T1 Free Run Warning (TTL)	NOT CHECKED	
57	T2 Lock Warning	>2.0V	
100	T5 Channel A ALC Voltage	<0V	3
Monitor P	oint Signal Checked	Tolerance	Severity
101	IF Receiver B ALC	NOT CHECKED	
102	IF Receiver C ALC	<0V	3
103	IF Receiver D ALC	NOT CHECKED	
104	IF Receiver A Level Monito	r <2.0V, >10.0V	2
105	IF RCVR B Level Monitor	NOT CHECKED	
106	IF RCVR C Level Monitor	<2.0V. >10.0V	2
107	IF RCVR D Level Monitor	NOT CHECKED	
110	+5 Volts Power Supply	<4.95V, >5.25V	1
111	-5 Volts Power Supply	NOT CHECKED	
112	+15 Volts Power Supply	NOT CHECKED	
113	-15 Volts Power Supply	NOT CHECKED	
114	+28 Volts Power Supply	<6.90V. >7.10V	1
115	-28 Volts Power Supply	<-7.1V, >-6.9V	1
120	TO TOTAL Power- Ch A	NOT CHECKED	
121	T6 Total Power -Ch B	NOT CHECKED	
122	T6 Total Power- Ch C	NOT CHECKED	
123	r6 Total Power- Ch D	NOT CHECKED	

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