SOCORRO, NEW MEXICO
VERY LARGE ARRAY PROGRAM

VLA ELECTRONICS MEMORANDUM NO. 194

THE LINEARITY OF THE DETECTOR IN MODULE F4
A. R. Thompson

August, 1980

## I. Introduction

The detector in the F 4 module is used to measure the ratio of the system noise temperature to the temperature of the switched noise source which is injected at the front end inputs for calibration purposes. In normal operation, a gated alc loop in the F4 holds the level of the system noise to a constant value, and the periodic increases when the calibration noise source is on are measured by means of a synchronous detector. The output of the synchronous detector should be proportional to the system gain, and it is used to correct the visibility data for gain changes that result from changes in antenna temperature and the action of the alc loop. It is particularly important to correct for any change of gain between
observation of $a$ source that is being mapped and observation of a calibration source. This calibration system is described in more detail in VLA Technical Report No. 40 by L. R. D'Addario.

A design modification introduced in July, 1980 (change order no. 166) has resulted in a decrease of 3.5 dB in insertion loss of the mixer in F 8 that converts the frequency from 1025 MHz to 1325,1425 , 1575 or 1675 MHz . The signal from F 8 goes to the detector in F4, and the alc attenuator acts on the 1025 MHz signal before it reaches F8 (see Fig 1.1 in VLA Technical Report No. 40). It is desirable to keep the operating point of the alc attenuator unchanged since this has been chosen to minimize phase changes. An increase of 3.5 dB in the signal from F 8 to T 2 is acceptable, and the simplest solution is to decrease the gain of the amplifier following the detector in F 4 so that, with the alc loop operating, the power level to the detector is -19 dBm rather than -22.5 dBm as originally designed. This is option (c) in a memorandum by L. R. D'Addario dated June 19, 1980 which is appended to this memorandum. The measurements described here were undertaken to check that the detector is sufficiently linear in its power-law response with the 3.5 dB increase in its input level.

## II. The Detector Linearity Requirement

With the switched noise source off the alc holds the detector output voltage at a value $v_{0}$ for which the detector input power is $p_{0}$. When the noise source is on the alc gating circuit holds the gain constant and the corresponding values of detector power and voltage are $p_{0}+\Delta p_{1}$ and $v_{0}+\Delta v_{1}$. The synchronous detector measures $\Delta v_{1}$, and we assume that $\Delta v_{1}$ is proportional to $\Delta \mathrm{p}_{1}$ which is a measure of the system gain. Now suppose that $\Delta \mathrm{P}_{2}$ and $\Delta \mathrm{v}_{2}$ are the corresponding quantities measured at some different time. The change in gain is $\Delta p_{1} / \Delta p_{2}$, and we want to know how much this differs from $\Delta v_{1} / \Delta v_{2}$.

Let the detector characteristic be given by $p=f(v)$.
Then

$$
\begin{equation*}
\frac{\Delta p_{1}}{\Delta v_{1}}=\frac{f\left(v_{0}+\Delta v_{1}\right)-f\left(v_{0}\right)}{\Delta v_{1}} \tag{1}
\end{equation*}
$$

When the antenna is looking at cold sky near the zenith, the system temperature is at its minimum and the magnitude of the calibration signal is such that $\Delta p_{1} / p_{0} \cong 0.1$. Suppose that in the next situation a large increase in antenna temperature has occurred so that there has been a large decrease in gain and $\Delta p_{2}$ is small

$$
\begin{gather*}
\frac{\Delta p_{2}}{\Delta v_{2}}=\left(\frac{d p}{d v}\right) v_{0}  \tag{2}\\
\frac{\Delta p_{1}}{\Delta p_{2}}=\frac{\Delta v_{1}}{\Delta v_{2}} \quad \frac{f\left(v_{0}+\Delta v_{1}\right)-f\left(v_{0}\right)}{\Delta v_{1}\left(\frac{d p}{d v}\right) v_{0}} \tag{3}
\end{gather*}
$$

This situation represents the largest error in gain change that is likely to occur as a result of the detector linearity assumption.

Figure 1 shows the expected form of $f(v)$, from which it can be seen that for a small departure from linearity $f(v)$ can be approximated by a linear term plus a quadratic term. Measured values of $p$ and $v$ were therefore analyzed by a polynomial fitting routine rather than attempting to read a graph of the points to the required accuracy. In fitting the polynomial it is convenient to center the measured region on the origin, which in this case tends to minimize the higher order coefficients. For a second order polynomial,

$$
\begin{equation*}
\left(p-p_{0}\right)=a+b\left(v-v_{0}\right)+c\left(v-v_{0}\right)^{2} \tag{4}
\end{equation*}
$$

and from (3) and (4)

$$
\frac{\Delta \mathrm{p}_{1}}{\Delta \mathrm{p}_{2}}=\frac{\Delta \mathrm{v}_{1}}{\Delta \mathrm{v}_{2}}\left(1+\frac{\mathrm{c} \Delta \mathrm{v}_{1}}{\mathrm{~b}}\right)
$$

The overall goal for accuracy of measurement of visibility data is usually quoted as $1 \%$. The contribution resulting from the detector characteristic should not be more than half of this, i.e. we require $\left|c \Delta v_{1} / b\right|<0.005$.
III. The Detector Measurements

Measurements were made using the F4 test setup in the Front End Lab, which is essentially the system shown in Figure 4.2 of VLA Technical Report No. 40, except that the CW signal source can be replaced by a
broadband noise source, which was the case in these measurements. Also the 10 dB pad between J1 of F4 and the isolated power divider was replaced by 7 dB to simulate the lower loss in the modified F8, The detector output voltage was measured at the Total Power BNC output on the front panel of the F4 using a Data Precision 245 digital voltaeter. The detector output has been amplified by one stage of dc gain at this point. The power level into the detector was measured at one port of the $1-2 \mathrm{GHz}$ isolated power divider using a General Microwave 460 B Power Meter with 1 mW head (N422C, Ser. 212715). The power meter output was read using a second Data Precision 245. Integrated circuit A10, a solid state switch (AD7512), was removed from the $F 4$ printed circuit board, to deactivate the alc loop and put the alc attenuator into the minimum attenuation condition.

The gain of the stage following the detector was adjusted by means of a trimpot on the circuit board for an output voltage close to 3.00 v for an input power of -19.5 dBm . These are the nominal values of $v_{0}$ and $p_{0}$. Measurements of $v$ and $p$ were taken at $\frac{1}{2}$ volt intervals in $v$ from $1.5 v$ to $6 v$. It is clearly important to measure over a wide enough range to detect any nonlinearity, but not so wide a range that departures from linearity that are outside the normal operating range affect the results. The range of the power meter was
not changed during the measurements. Measurements were repeated two or more times with decreasing and then increasing power, and averaged to reduce drifts when the results did not repeat accurately. The voltage readings were stable to about $0.2 \%$. A different power meter, HP 435A with 8482A head, was tried but the General Microwave appeared to give better accuracy. Measurements were made on three $\mathbf{F 4}$ modules, serial numbers C72, C46 and C77. The values of $p$ and $v$ with $v$ nearest to 3 volts were subtracted from the other values in each case, and analyzed using the polynomial regression routine on an H.P. 9830 calculator. The results for the three modules are given in the three following pages. In normal operation $\Delta v$ is $10 \%$ of $v_{0}$, i.e. 0.3 volts, so the $0.5 \%$ gain accuracy requirement was met by all three modules.

The results do not show any consistant values for the second order coefficient. Furthermore, if one substitutes into equation (4) values of $\left(v-v_{0}\right)=1$ volt, $b=.035, c=.0001$, the contribution of the second order term is roughly similar to that of an uncertainty of $0.2 \%$ in $v$. This suggests that the second and higher order coefficients in the polynomial solutions largely represent the measurement errors rather than the detector characteristics.



Figure D Deteetor characteristic represented by a iniear
torm plus a seend rdes arm in (Vvo)


HO．POTHTS $=8$




COEFFICTENTS
$6^{t h}$ degree solution


R GOURRE $=0.999984024$

$$
\begin{aligned}
& \mathrm{S}=1.99 \quad \text { UHT D. De9006ege }
\end{aligned}
$$

$$
\begin{aligned}
& \mathrm{X}=0 \quad \mathrm{HHT}=2.926 \mathrm{E}-\mathrm{G4} \\
& \mathrm{~K}=-1.001 \quad \text { H月T }=-0.094955615 \\
& \mathrm{~S}=-1.498 \quad \text { U月T-0.05090656 }
\end{aligned}
$$

COEFFTUTEUTS



HO. POINTS $=10$
\%: MEAN $=8.7526$
ST. DEV $=1.517465476$
Y: MEAH $=0.02581$
ST.DEV: $=0.0525959$

CORR.COEFF: $=0.99999567$



## National Radio Astronomy Observatory

Charlottesville, Virginia

| To: | P. Napier, P. Lilie, H. Richards, <br> J. Campbell, G. Barrell |
| :--- | :--- |
| From: June 19, 1980 |  |
| Larry R. D'Addario |  |

Subject: F8 Modifications

New F8 modules will incorporate several modifications which should give much more repeatable performance from unit to unit and provide better input match. The modified design will be designated series B; the first unit of the series, F8-B23, was shipped to you yesterday.

Details of the internal changes will be sent to you shortly in a set of Change Orders, but you should immediately be aware of resulting changes in the external specifications and their consequences for the system. These specification changes are (cf. VLA Tech. Report No. 40, p. 10):

1. 50 MHz comb line levels needed to ensure that specs will be met are:

| Line | New Spec | Previously |
| :---: | :--- | :---: |
|  |  |  |
| 300 MHz | $-7 \pm 1.5 \mathrm{dBm}$ | $-6 \pm 2 \mathrm{dBm}$ |
| 400 MHz | $-8.5 \pm 1.5$ | $-9 \pm 2$ |
| 550 MHz | $-9.0 \pm 1.5$ | $-9 \pm 2$ |
| 650 MHz | $-12.0 \pm 1.5$ | $-12 \pm 2$ |
| Total power <br> in comb | $\leq+4$ |  |

These correspond to levels actually measured in system tests on antennas 14 through 23, so no system changes should be needed provided that all other antennas conform.
2. The conversion loss has been reduced from $13.5 \pm 1.5 \mathrm{~dB}$ to $10 \pm 1 \mathrm{~dB}$. Note that the F8 output level is determined by the set point of the ALC loop in F4. Thus, if the latter set point is unchanged and if the $F 4$ input level from the front end is unchanged, the ALC attenuator will operate with 3.5 dB more loss than before, on the average. This is highly undesirable. We have several options:
a) Change the F 4 module open loop gain from $66 \pm 1.5 \mathrm{~dB}$ to $62.5 \pm 1.5 \mathrm{~dB}$ by replacing the selected pad in the module. This would be expensive and time consuming (because a complicated test setup is needed), but would be straightforward.
b) Change the standard front end gain from $61 \pm 1 \mathrm{~dB}$ to $57.5 \pm 1 \mathrm{~dB}$, thus reducing the F 4 input level. This has the disadvantage that it cuts into F4's input noise figure margin, which is now about 10 dB .

Memo to: P. Napier, P. Lilie, H. Richards, J. Campbell, G. Barrell

June 19, 1980
Page 2
c) Increase the F8 output level by 3 dB by changing the ALC loop set point in $F 4$. This has the disadvantage that the detectors in $F 4$ are less accurately square-law at the higher level, but it looks like the accuracy would still be acceptable. Note that this will affect T2/T1.

You should choose one of these options. I strongly advise against any "temporary" choice; make a definite decision and stick to it! If a) or c) is chosen, I will make appropriate changes in the F4/F8 documentation; please let me know as soon as possible.
3. The output power delivered to $T 2$ cannot be held to as tight a tolerance as I had planned, due to component tolerances. I had expected $\pm 0.5 \mathrm{~dB}$ for each channel; it now looks like $\pm 1 \mathrm{~dB}$ can be held, but $I$ would be prepared to guarantee only $\pm 1.3 \mathrm{~dB}$. (The nominal level is $-34.0 \mathrm{dBm} /$ channel, but would become -31.0 if option $c$ ) above is adopted.) This must be taken into account in the modem gain budget.

