VLBA Acquisition Memo # 75

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Area Code 617 692-4765

See Acq.

To: VLBA Data Acquisition/Recorder Group

From: Alan E. E. Rogers

Sujbect: VLBA Baseband Converter Tests - Status of Performance Verification II

Item	Specification	Measured Performance	<u>Memo</u> #
Gain through conv (2 MHz BW):	64 <u>+1</u> dB Max	63.5 dB	64
Level control phase shift:	<0.5 deg	<l deg<="" td=""><td></td></l>	
Image rejection:	>26 dB	>26 dB	61
Energy in 10 KHz sidebands:	<40 dBc	~-45 dBc	61
L.O. phase noise:	<2 deg rms	<l deg="" rms<="" td=""><td>7Ø</td></l>	7Ø
L.O. leakage into video:	<-50 dB	-80 dBm at 500 MHz	64
Gain compression:	<1%	<Ø <b>.</b> 5%	
Noise temperature:	<100,000 deg K	~100,000 deg K	
Temp. coeff of phase:	<l c="" deg="" ghz<="" td=""><td>-Ø.4 deg/deg C/GHz</td><td>67</td></l>	-Ø.4 deg/deg C/GHz	67
L.O. settling:	<l sec<="" td=""><td>&lt;300 ms</td><td>64</td></l>	<300 ms	64
L.O. leakage into input:	<-60 dBm	-73 dBm at 500 MHz	64
Temp. coeff of gain:	<0.1 dB/deg C	-0.05 dB deg C	74
Temp. coeff delay:	<0.1 ns/deg C at 8 MHz B	W ~0.2 ns/deg C	67
4-Way input switch isolation:	<60 dB	~65 dB	61
Frequency range:	500 - 1000 MHz	490 - 1050 MHz	7Ø
Bandpass response:			
Gain variation within			
80% of band:	<0.5 dB	<Ø.4 dB	74
Response at 108% of band:	<-10 dB	<-15 dB	74
Instrumental closure:	<0.1 deg	<0.03 deg (est.)	74

## Notes:

## 1] Level Control Phase Shift

The digital gain control produces a small phase shift with level change. Phase variations are within 0.5 degrees of the mean at 1 MHz over a 30 dB range. However, phase variations increase to 1.5 degrees at 8 MHz. Most of the variation has been traced to cross-coupling on the pc board. An improved pc layout should bring phase variations within specification for all bandwidths.

## 2] Gain Compression

The square law linearity (after subtraction of the zero offset - which is typically  $\emptyset.5$ % of full scale) is better than  $\emptyset.5$ %. The 1 dB compression point of the video output is +15 dBm (15 dB above the normal output).

#### 3] L.O. Phase Noise and Frequency Range

The new oscillator pc board has improved the frequency range so that 500-1000 MHz is now covered without difficulty. The problem of high phase noise in some baseband converters has been solved by changing the FET bias current. Details are given in Acquisition Memo #70.

## 4] Noise Temperature

The noise temperature of the IFD/baseband converter has been improved by using mixers at a higher impedance (100 ohms).

#### 5] Bandpass Response and Stability (See Acquisition Memo #74)

Several improvements have recently been made in the bandpass response, matching and stability. Instrumental closure phase error is estimated from measurements of the phase and amplitude match between bandpass responses.

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