

Mail for Ray Escoffier

Mon, 26 Apr 1999 08:31:00 -0400

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From: GAIL.RUCKER@avnet.com
To: rescoffi@polaris.cv.nrao.edu
Subject: Re: Xilinx chip prices
Date: Mon, 26 Apr 1999 08:31:00 -0400

Hi Gail,

I need to get parts for my prototype logic card ordered. Can you get me prices and estimated delivery for the Xilinx parts below;

quan	part		
7	XCV50-5 PQ240C	\$111.00	12 WEEKS
8	XCV600-6 HQ240C	\$2276.00	16 WEEKS
	XCV100-5PQ240C	\$208.00	12 WEEKS

I don't remember when the XCV50 parts were suppose to be out but if they have a long lead time, I can buy XCV100 parts instead. Thanks.

Ray

Ray, I will give you a call this afternoon. (4/26)

Thanks,
Gail

\$18,985.00

Re: Xilinx chip prices

From: GAIL.RUCKER@avnet.com
To: (Ray Escoffier) <rescoffi@NRAO.EDU>
Subject: Xilinx chip design
Date: Wed, 17 Mar 1999 07:08:00 -0500

Ray, here is the requested pricing:

1.	XCV300-6PQ240C	8192 PIECES	CURRENT	\$ 342.00
			JUNE '99	\$ 147.00
2.	XCV600-6HQ240C	4096 PIECES	CURRENT	\$1323.00
			JUNE '99	\$ 396.90

Thank you for the opportunity to provide this quote.

Gail

----- (Forwarded letter 1 follows) -----

Date: Thu, 11 Mar 1999 10:06:25 -0500 (EST)
To: kroberson@hh.avnet.com
Cc: GAIL.RUCKER
From: Ray.Escoffier[rescoffi@NRAO.EDU]
Sender: rescoffi@polaris.cv.nrao.edu
Subject: Xilinx chip design

Hi Ken,

For your information, I have succeeded in fitting my 128 tap filter design into a XCV300-6 chip. The Xilinx software gives 133.6 MHz as the highest operating clock rate, so it meets my goal of a 125 MHz system clock. Thanks for your assistance!

I still have one problem however, I tried to clean up my schematic by making a

MACRO from some of the logic but I find now that when I get so many elements on my schematic, the software bombs with an SC Application Error (in module SU_UTIL.DLL, if that means anything). I get this error using either a XC4062XLA or a XCV300 chip. (I put a MACRO inside a MACRO, is that OK?).

I down loaded the latest upgrades for the Foundation package from the Xilinx web site, but that did not help the problem above.

My next task is to use the chip above to design a prototype filter card. To get to the full 4 GHz clock rate of the input to be filtered, I will need 16 of the XCV300 chips. An alternative would be to put twice the logic of the design above and require only 8 chips on the card.

Can I get some budgetary price estimates so I can decide between the 16-chip and the 8-chip alternatives?

The final system would require 512 filter cards, so the Xilinx chip breakdown would be;

8192 XCV300 chips for the 16-chip design or
4096 XCV600 chips for the 8-chip alternative

Could you get me some budgetary cost estimates for the conversion of the XCV300 and the XCV600 chips to a HardWire implementation and chip prices for the HardWire chips at the quantities above?

Again, thanks for your help.

From: GAIL.RUCKER@avnet.com
To: (Ray Escoffier) <rescoffi@NRAO.EDU>
Subject: Re: Xilinx chip prices
Date: Tue, 27 Apr 1999 07:55:00 -0400

Ray, the price breaks are at 25, 100, and 1000. Also, we do not have a definite word on any price reductions but we will let you know when we do.

Please let me know of any additional information that would be of benefit to you.

Thanks,
Gail

----- (Forwarded letter 1 follows) -----
Date: Wed, 21 Apr 1999 15:08:43 -0400 (EDT)
To: GAIL.RUCKER
From: Ray.Escoffier[rescoffi]@NRAO.EDU
Sender: rescoffi@polaris.cv.nrao.edu
Subject: Re: Xilinx chip prices

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