

MASSACHUSETTS INSTITUTE OF TECHNOLOGY
HAYSTACK OBSERVATORY
WESTFORD, MASSACHUSETTS 01886

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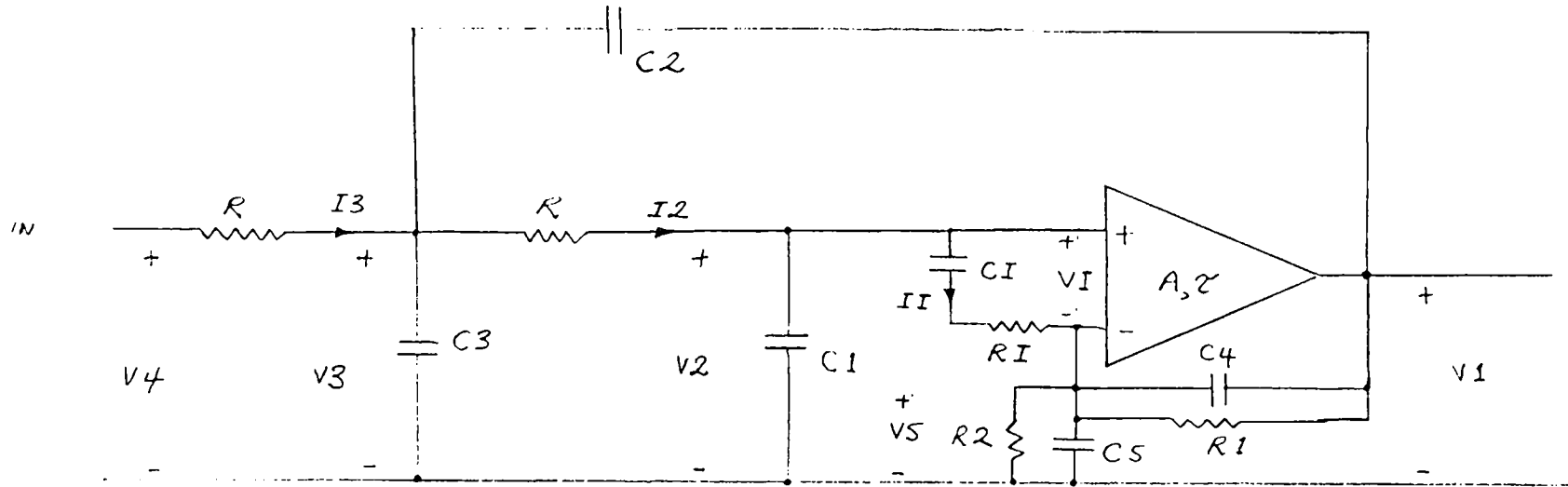
To: VLBA Data Acquisition/Recorder Group
From: Alan E.E. Rogers
Subject: Active Filter — Sensitivity to Operational Amplifier Performance

The active filter in the VLBA baseband converter has been analyzed on paper and using a simple computer program to simulate the sensitivity to various circuit parameters.

Figure 1 shows the circuit model used for computer simulation. Only the high Q ($Q = 2.56$) pole pair simulation is shown since the low Q poles are much less sensitive to circuit parameters. At frequencies below 4 MHz the circuit is insensitive to variations in the operational amplifier gain (since the gain is high and phase shifts are small) and variations in stray capacitance C_5 . At 16 MHz bandwidth the circuit is quite sensitive to variations in C_5 and the operational amplifier gain and delay. The printouts show the sensitivity to various parameter changes. Performance at 16 MHz bandwidth could be improved by using an amplifier with more gain and less delay but at present I have not been able to find anything better than the Signetics NE5539 available at reasonable cost. The lowering of the pole frequencies at 8 MHz bandwidth is compensated by the choice of resistor value for the most significant bit (used for 8 and 16 MHz). The 16 MHz bandwidth, however, is significantly reduced from the nominal 14.4 MHz - see Figure 2 for measured bandpass printout.

Attachment (7 pages)

(E) ACTIVE FILTER MODEL USED IN PRG. ACTFL (AER Aug 86)



Transfer function ($V1/V4$)

$C3$ simulates switch + circuit board capacitance

$C1, R1$ Part of H.F. compensation

$C5$ - stray capacitance

A is frequency dependent gain modelled with freq. dependent amplitude plus delay

values:

$R1 = 50\Omega$	$C3 = 60pF$
$C1 = 50pF$	$R = 50\Omega$ at 16 MHz BW
$C5 = 4pF$	$ A = 30dB$ at 16 MHz, 36 dB at 8 MHz
$R2 = 2.7K$	$\zeta = 20 ns$
$R1 = 200\Omega$	
$C4 = 150pF$	
$C1 = 43pF$	
$C2 = 1000pF$	

Figure 1.

(2)

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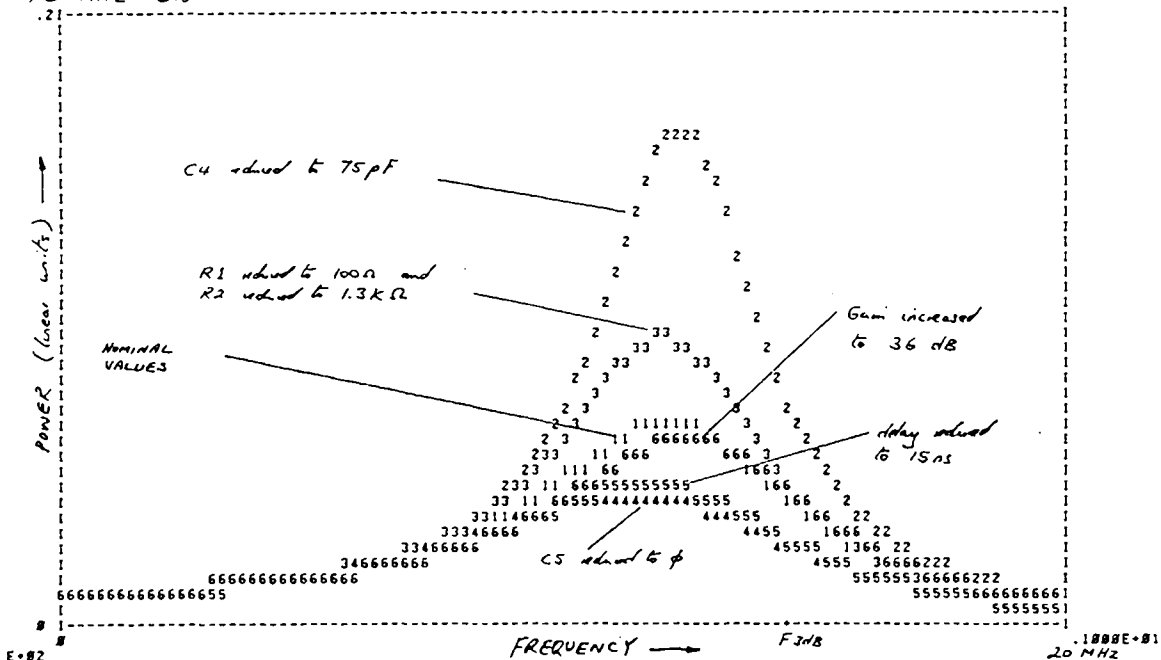
1   FTN66,Y
2   PROGRAM ACTFL
3   COMPLEX AI1,AI2,AI3,V1,V2,V3,V4,V5,Z5,Z4,ZI,AA,AII,VI,CONJG
4   CALL RMPAR(IPAR)
5   LU=IPAR
6   PI=3.1415926536
7   DO 5000 IR=1,9
8   IF(IR.GT.2.AND.IR.LT.9)GOTO 5000
9   CALL VGRPH(0.0,1.0,00.0,20.0,1)
10  DO 2100 I=1,6
11  RI=47
12  CI=50E-12
13  C1=43E-12
14  C3=60E-12
15  C2=1133E-12 -C3
16  R=50*(2**FLOAT(IR-1))
17  C4=150E-12
18  C5=4E-12
19  C   R1=470
20  R1=200
21  C   R2=12000
22  T=20E-9
23  AAA=30*IR
24  IF(I.EQ.2) C4=C4*.5
25  IF(I.EQ.3) R1=100
26  IF(I.EQ.4) C5=0.0
27  C   IF(I.EQ.5) CI=C1*2.0
28  C   IF(I.EQ.5) RI=25
29  IF(I.EQ.5) T=15E-9
30  IF(I.EQ.6) AAA=AAA*2.0
31  R2=R1*27*.5
32  DO 2000 IW=1,100
33  W=(IW-1)*20E6*2.0*PI/(100.0*2**(IR-1.0))
34  V1=CMPLX(1.0,0.0)
35  ZI=CMPLX(RI,-1.0/(W*CI))
36  Z5=CMPLX(1.0,0.0)/(CMPLX(0.0,W*C5)+CMPLX(1.0/R2,0.0))
37  Z4=CMPLX(1.0,0.0)/(CMPLX(0.0,W*C4)+CMPLX(1.0/R1,0.0))
38  VI=V1/CMPLX(AAA*COS(W*T),-AAA*SIN(W*T))
39  AII=VI/ZI
40  V5=V1*Z5/(Z4+Z5)+AII*(Z4*Z5)/(Z4+Z5)
41  V2=V5+VI
42  AI2=V2*CMPLX(0.0,W*C1)+AII
43  V3=V2+AI2*CMPLX(R,0.0)
44  AI3=AI2+V3*CMPLX(0.0,W*C3)+(V3-V1)*CMPLX(0.0,W*C2)
45  V4=V3+AI3*CMPLX(R,0.0)
46  AMP=CABS(V1*V1/(V4*CONJG(V4)))
47  WW=(IW-1)/100.0
48  II=48+I
49  CALL VGRPH(WW,AMP,II,3,2)
50  2000 CONTINUE
51  WRITE(6,1200) R,C3,C4,C5,R2,AAA,T,CI,RI
52  2100 CONTINUE
53  CALL VGRPH(0.0,1.0,00.0,20.0,4)
54  CALL VGRPH(0.0,0.0,6,5)
55  1200 FORMAT( " R,C3,C4,C5,R2,AAA,T,CI,RI=",9E10.3)
56  5000 CONTINUE
57  END

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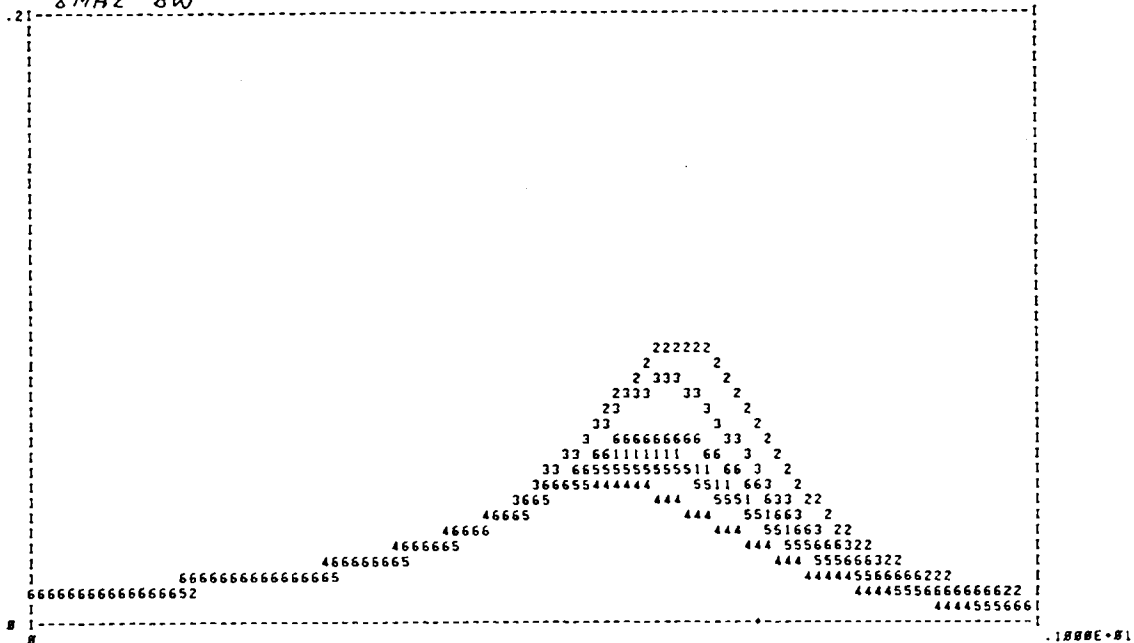
EOF on FMGR file &ACTFL after reading 57 records with approx 1522. chars

R,C3,C4,C5,R2,AAA,T,CI,RI= .500E+02 .600E-10 .150E-09 .400E-11 .270E+04 .300E+02 .200E-07 .500E-10 .470E+02

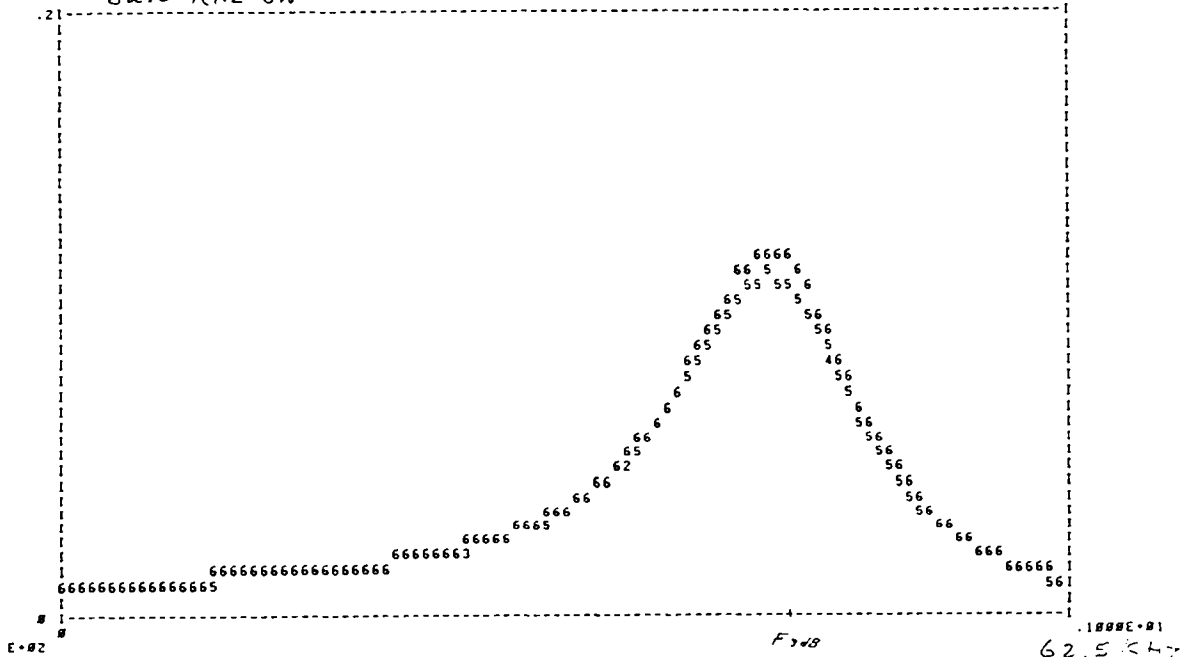
16 MHz BW



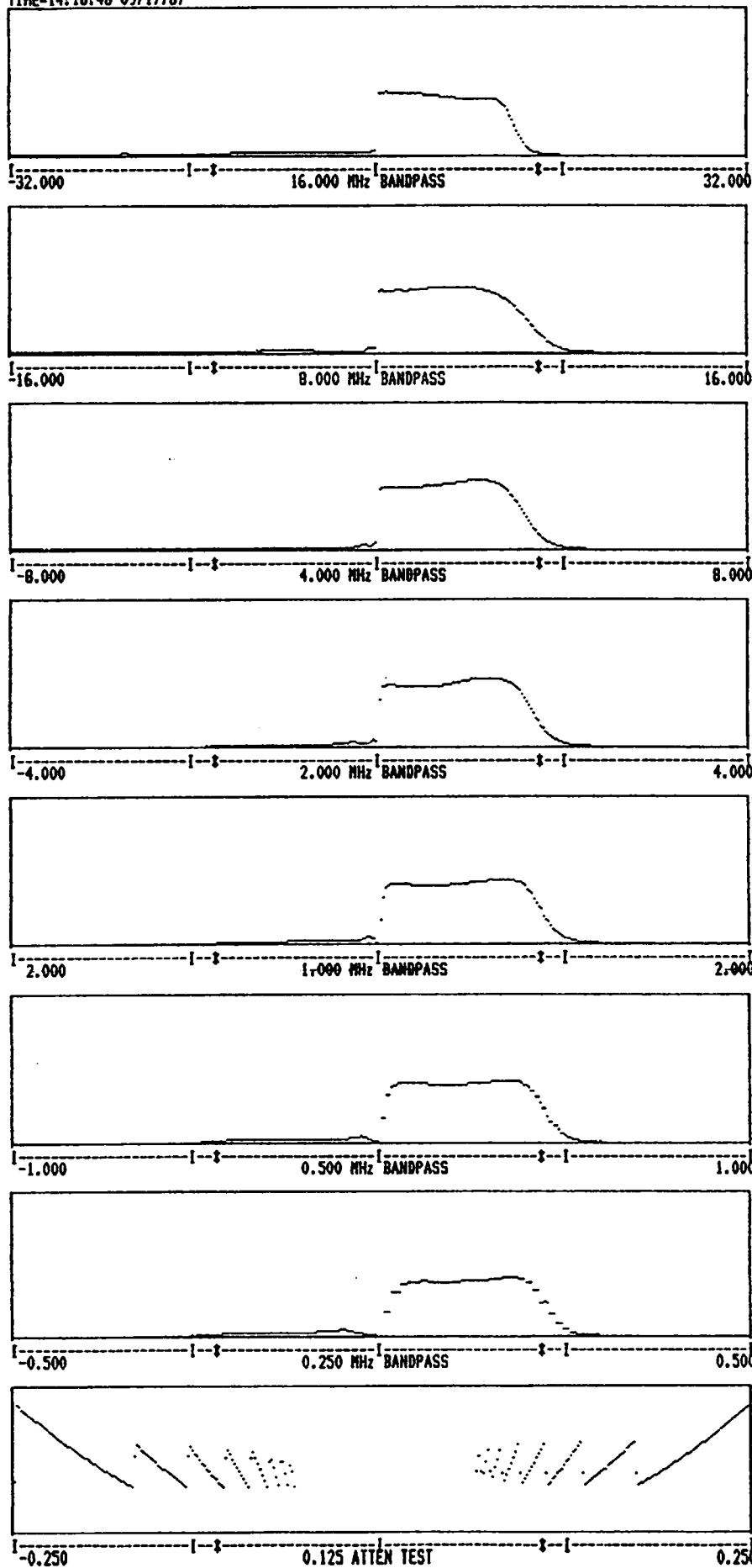
8 MHz BW



62.5 KHz BW



REJECTED SIDEBAND SCALED UP BY 10 dB
TIME=14:16:48 09/17/87



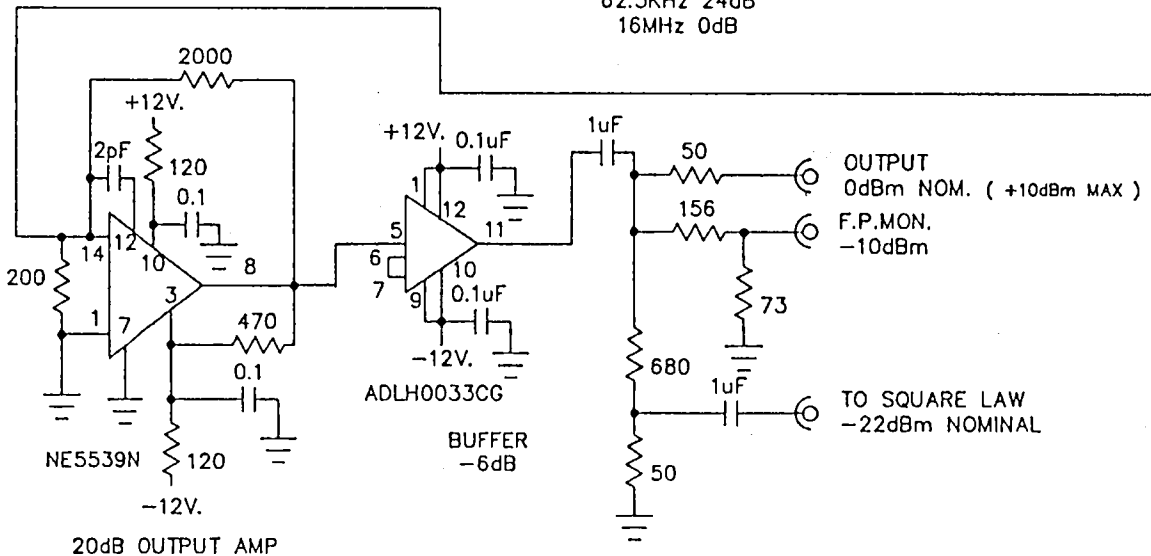
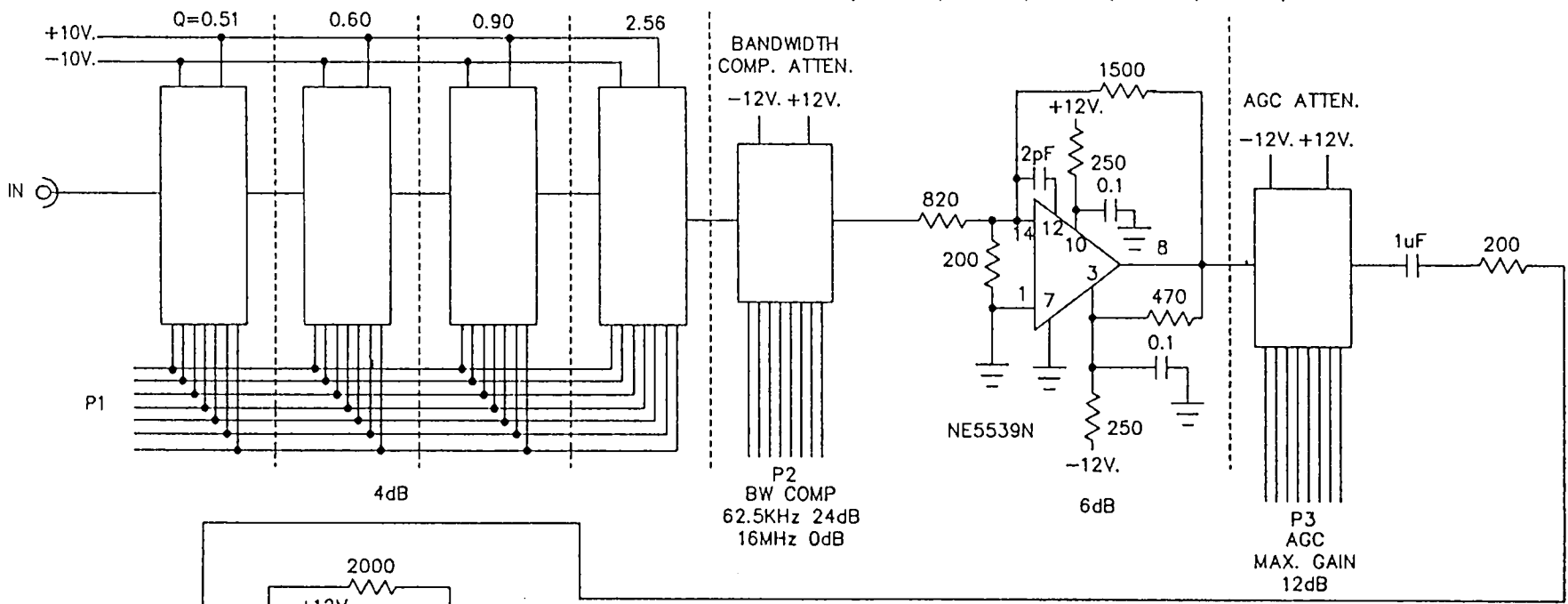
REJECTED SIDEBAND SCALED UP BY 10 dB
TIME=16:53:12 09/17/87

Figure 2
(4)

(5)

CHANGE LETTER	DWN BY	CHK'D BY	APP'D BY	DATE	D.C.N. & DESCR

ACTIVE FILTER SECTIONS



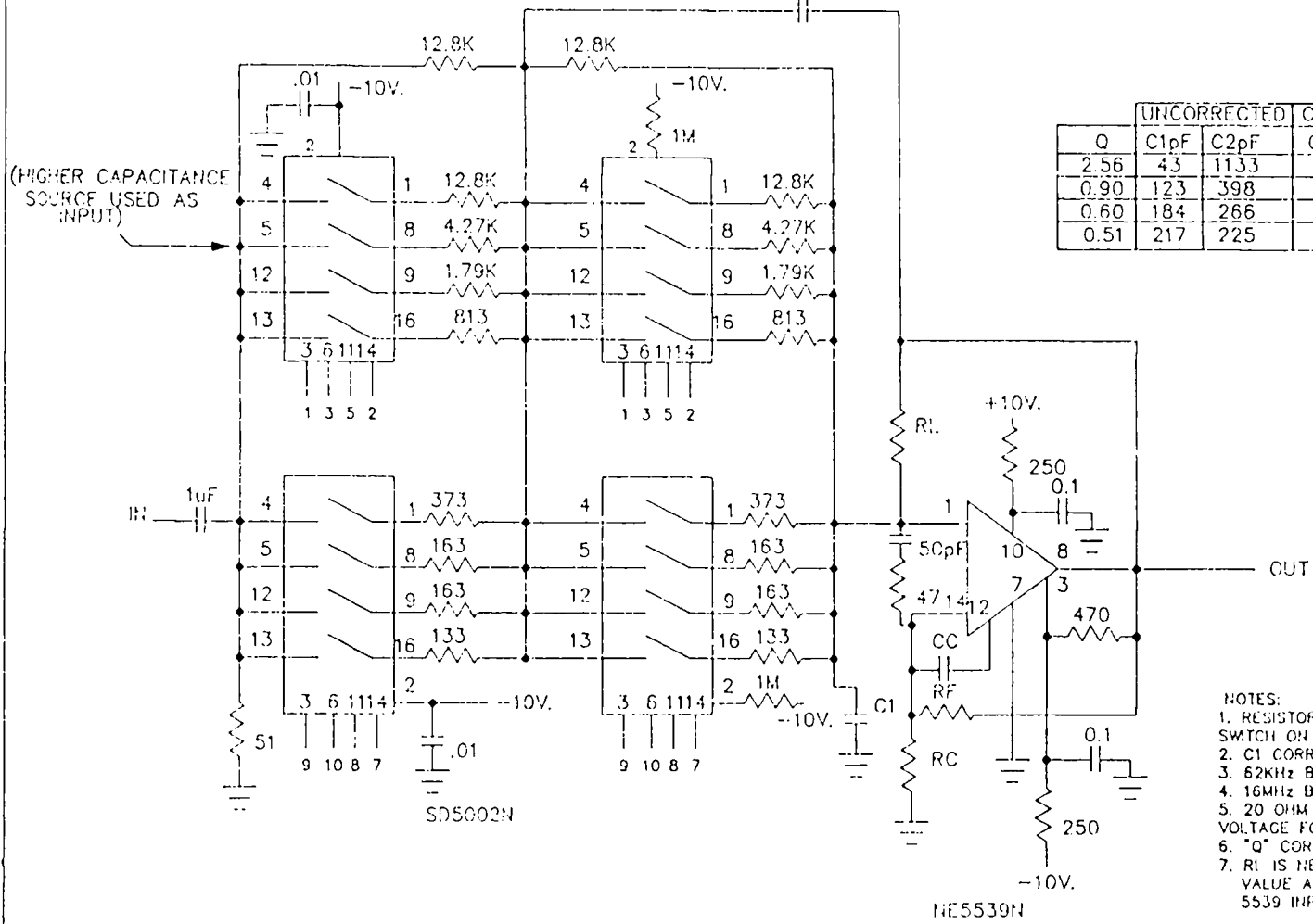
NOTES:

1. DASHED LINES INDICATE SEPARATE PC BOARD SECTIONS.
2. SEE DRAWINGS 54120S012, 54120S001, AND 54120S004 FOR CIRCUITS OF ACTIVE FILTER, BANDWIDTH COMP., ATTN. AND AGC.

ELECTRONIC NOTES: UNLESS OTHERWISE NOTED: RESISTORS: CAPACITORS: INDUCTORS:	USED ON	DRAWN FOR: A.E. ROGERS	DATE: 9-85	NORTHEAST RADIO OBSERVATORY CORPORATION HAYSTACK OBSERVATORY WESTFORD, MASSACHUSETTS			
		DRAWN BY: A. PHILBROOK	9-85				
		CHECKED BY:		BASEBAND FILT/AMP (BASEBAND FILTERS & BW GAIN & AGC)			
SCALE	APPROVAL	PROJECT		C	54120S003		
CLASSIFICATION		ENGINEER:		AER\FILTAMP	DWG. SIZE	DWG. NO.	REV.

C-54120S003

CHANGE LETTER	DRW BY	CHK'D BY	APP'D BY	DATE	D.C.H. & DESCRIPTION



- NOTES:
1. RESISTOR VALUES CORRECTED FOR 40 OHMS SWITCH ON RESISTANCE.
 2. C1 CORRECTED FOR 20pF TO GROUND.
 3. 62KHz BW ALL INPUTS LOW.
 4. 16MHz BW ALL INPUTS HIGH.
 5. 20 OHM RESISTORS (ON INPUT SECTION) DROP SUPPLY VOLTAGE FOR ALL FOUR SECTIONS.
 6. "Q" CORRECTED FOR 40pF TO GROUND USING "RC".
 7. R1 IS NEEDED ON Q = 2.56 SECTION VALUE APPROX. 20K TO COMPENSATE FOR 5539 INPUT RESISTANCE.

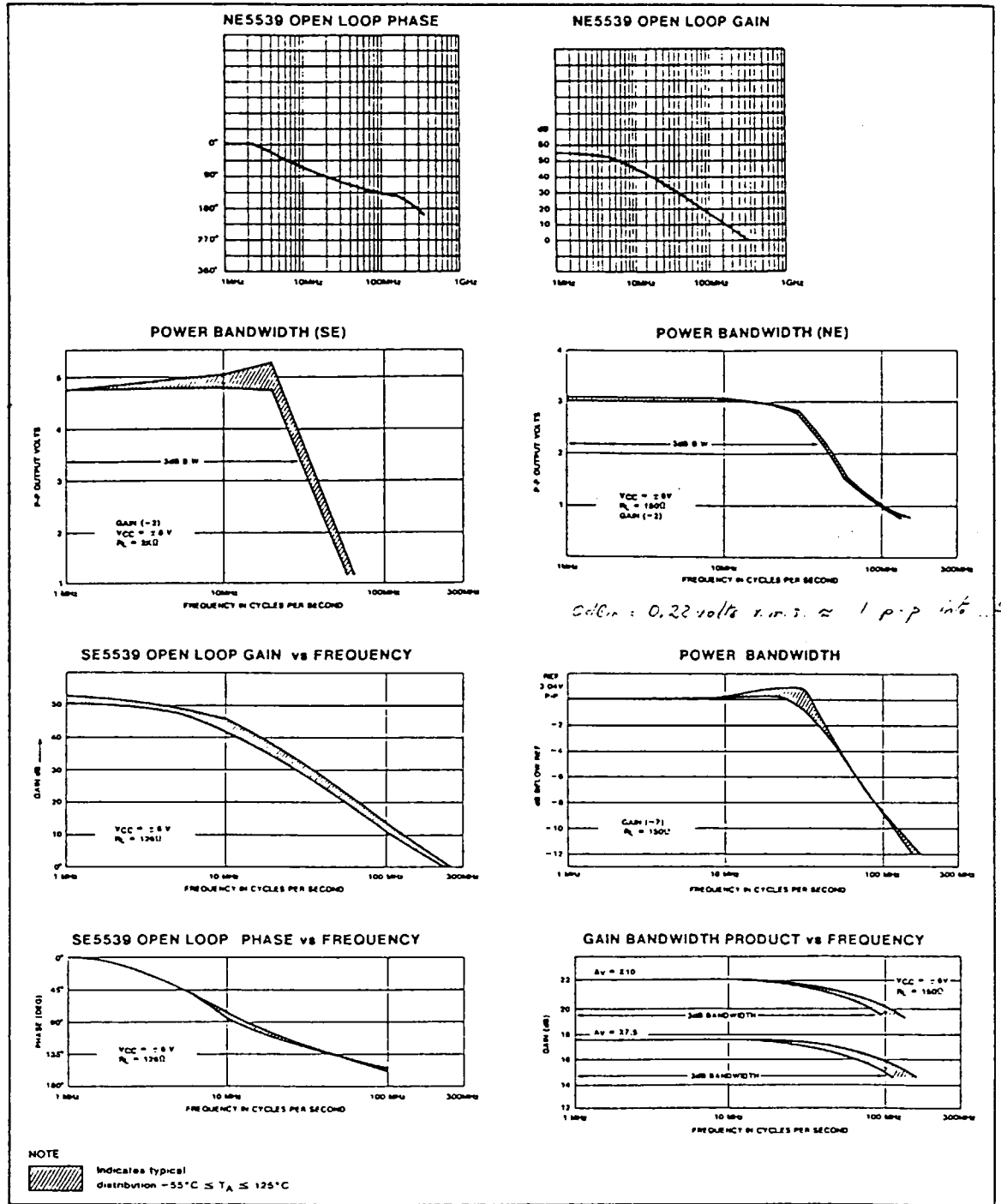
NOTES:

ELECTRONIC NOTES: UNLESS OTHERWISE NOTED	USED ON	DRAWN FOR: A. EROGERS	DATE: 8-85	NORTHEAST RADIO OBSERVATORY CORPORATION HAYSTACK OBSERVATORY WESTON, MASSACHUSETTS			
		DRAWN BY: A. PHILBROOK	8-85	BASEBAND CONVERTER ACTIVE FILTER SECTION			
RESISTORS:	SCALE	CHECKED BY:	PROJECT ENGINEER		C	54120S012	
CAPACITORS:	CLASSIFICATION				OWS SIZE	DWG NO	REV
INDUCTORS:							

C - 54120S012

ULTRA HIGH FREQUENCY OPERATIONAL AMPLIFIER

NE/SE5539



17-12

Signetics

(7)