

VLBA ACQUISITION MEMO #206

MASSACHUSETTS INSTITUTE OF TECHNOLOGY

HAYSTACK OBSERVATORY

WESTFORD, MASSACHUSETTS 01886

14 June 1990

Area Code 508

692-4764

To: VLBA Data Acquisition Group
From: Alan E.E. Rogers
Subject: Minutes of VLBA Recorder Telecon Held 13 June 1990 at 1300 EDT
Attendees: Durga Baghri - Socorro
Jack Campbell - Socorro
George Peck - Socorro
Alan E.E. Rogers - Haystack

Monitor Board Upgrade

Ed Nesman will redesign the monitor board to use the AT&T clock recovery chip. This will eliminate the need for the high frequency "X21" reference. This change will simplify the board and do away with the plug in ECL board associated with the X21 clock.

Jack Campbell expressed the opinion that we should eventually retrofit all the boards to make them all the same.

It is anticipated that the new design will be complete this summer and that the 1991 recorder production will have the new boards.

Other Recorder Mods

George Peck and Jack Campbell will meet with others in Socorro to discuss other minor recorder changes which are desirable.

Thin Tape Order

The Sony order should be placed next week and the purchase order to Maxell and Ampex are being sent to Charlottesville for approval.

Sampler D.C. offsets

None of the DAR samplers have been specially trimmed to zero out the D.C. offset. Without trimming, the D.C. offset could be as high as 5 mV out of a 220 mV signal or a correlation coefficient of 5×10^{-4} . Software correction is possible and Alan Rogers will discuss with Jon Romney.