

# VLBA ACQUISITION MEMO #212

MASSACHUSETTS INSTITUTE OF TECHNOLOGY

HAYSTACK OBSERVATORY

WESTFORD, MASSACHUSETTS 01886

9 August 1990

Area Code 508

692-4764

To: VLBA Recorder Group

From: Alan E.E. Rogers

Subject: Minutes of VLBA Recorder Telecon Held 8 August 1990 at 1300 EDT

Attendees: R. Escoffier - NRAO, Charlottesville  
J. Romney - NRAO, Charlottesville  
D. Bagri - NRAO, Socorro  
B. Clark - NRAO, Socorro  
P. Napier - NRAO, Socorro  
G. Peck - NRAO, Socorro  
J. Webber - Interferometrics, Inc.  
A. Rogers - Haystack

## First Commercial VLBA Terminal

John Webber reported that the first DAR and REC built by Interferometrics is nearing completion. Tests made using Fuji H621 yielded parity error rates of a few in  $10^6$  and sync loss of a few in  $10^5$ . Final "zero-baseline" tests will be performed and processed on the Washington Correlator sometime next week. John also mentioned that he is cycling all the electronic modules in the DAR for -20 to +60 °C as a way of provoking infant mortalities which can then be fixed before the system is shipped.

## Thin Tape Plan

George Peck is developing a plan for the thin tape. This plan will include some basic testing of magnetic performance and abrasivity. Alan Rogers pointed out that a method has now been worked out for handling a mixture of thick and thin tapes on a processor transport (see VLBA Acquisition Memo #208). A special meeting will be held to discuss the plan.

## Data Buffer

This data buffer module was tested in July and the documentation has been sent to NRAO.

## Parallel Reproduce Electronics

Jonathan Hargreaves plans to commit this electronics to a PC board and no additional wire-wrap versions (beyond the two units already built for the VLBA) will be made. He is investigating a more highly integrated circuit design - but will probably stay with the existing circuit.